

	Type	Hits	Search Text	DBs
1	IS&R	485	(327/544).CCLS.	US-PGPUB; USPAT
2	IS&R	360	(327/546).CCLS.	US-PGPUB; USPAT
3	IS&R	1162	(327/530).CCLS.	US-PGPUB; USPAT
4	BRS	360	S5 and (enable active standby sleep)	US-PGPUB; USPAT
5	BRS	328	S6 and transistor	US-PGPUB; USPAT
6	BRS	1072	327/538.ccls.	US-PGPUB; USPAT
7	BRS	430	S8 and (enable active standby sleep) and transistor	US-PGPUB; USPAT
8	BRS	665	327/543.ccls.	US-PGPUB; USPAT
9	BRS	288	S10 and (enable active standby sleep) and transistor	US-PGPUB; USPAT
10	BRS	297	S10 and (enable active standby sleep)	US-PGPUB; USPAT
11	BRS	213	S12 not S9	US-PGPUB; USPAT
12	BRS	1492	327/540-541.ccls.	US-PGPUB; USPAT
13	BRS	617	S14 and (enable active standby sleep)	US-PGPUB; USPAT
14	BRS	546	S15 not S13	US-PGPUB; USPAT
15	IS&R	1123	(323/315).CCLS.	US-PGPUB; USPAT
16	BRS	326	S18 not (S8 S10 S14)	US-PGPUB; USPAT
17	BRS	443	S10 and resistor	US-PGPUB; USPAT
18	BRS	11461	mirror same (enable disable sleep active standby inactive)	USOCR; EPO; JPO; DERWENT; IBM_TDB
19	BRS	382	S22 same transistor	USOCR; EPO; JPO; DERWENT; IBM_TDB
20	BRS	439	S17 and (enable active standby sleep)	US-PGPUB; USPAT
21	IS&R	758	(327/427).CCLS.	US-PGPUB; USPAT
22	BRS	19771	current near2 mirror\$3	US-PGPUB; USPAT
23	BRS	1525	S30 with (enable active standby sleep) and transistor	US-PGPUB; USPAT